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APPLICANTS

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 Hisaaki Tominaga, Oizumi-machi, JAPAN; Hidetaka Sawame, Ota-city, JAPAN;** CONTINUING DATA ***** *AC*** FOREIGN APPLICATIONS ***** *AC*JAPAN 2000-397716 12/27/2000 *AC*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 4
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials					

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TITLE

Semiconductor device and manufacturing method thereof

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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